Comboctl
(control Combo6 card)

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The comboctl command displays or sets various variable in the Combo6 card bus (PCI) address space.

Synopsis:

comboctl [-d device] [-F config] [-Lqv] -a
[-d device] [-F config] [-Lqv] name ...
[-d device] [-F config] [-Lqv] -w name=value ...
[-d device] [-F config] [-Lqv] -X varfile ...
[-d device] [-F config] [-Lqv] -S script
-D

Typical use on command line:

```
comboctl -a
comboctl plx.mailbox0 fpga.cam.addr.w0
comboctl -w ports.port3.mii.bmcr.fdx=1 fpga.cam.addr.w0=0xa1
comboctl -S set_sram1.tcl
```

Comboctl scripting

Sample of comboctl.conf:

```
ctlvar fpga type bytestream width 0x8000 base pci.memory1
ctlvar fpga.cam offset 0x00
ctlvar fpga.cam.cmd   offset 0x2000 read_proc reg_read write_proc reg_write
ctlvar fpga.cam.val     offset 0x2004 read_proc reg_read write_proc reg_write
ctlvar fpga.cam.addr   offset 0x00 read_proc reg_read write_proc reg_write
ctlvar fpga.cam.addr.w0    offset 0x00    read_proc reg_read write_proc reg_write
ctlvar fpga.cam.addr.w1    offset 0x04    read_proc reg_read write_proc reg_write
.
.
.
ctlvar fpga.cam.data offset 0x400
ctlvar fpga.cam.data.w0    offset 0x48    read_proc reg_read write_proc reg_write
ctlvar fpga.cam.data.w1    offset 0x52    read_proc reg_read write_proc reg_write
.
.
.
```

```
proc reg_read { reg } {
	return [ combo_space_read $reg 0 ]
}

proc reg_write { reg val } {
	combo_space_write $reg 0 $val
	return
}
```

Sample of script for comboctl:

```
.
.
.
if { [comboget fpga.cmd ] == 0x01 } {
        set addr_addnet1 [ comboget fpga.cam.addr.w0 ]
        set addr_addent2 [ comboget fpga.cam.addr.w1 ]
        set val_addent1 [ combo_space_read fpga.cam.data $addr_addent1 ]
        set val_addent2 [ combo_space_read fpga.cam.data $addr_addent2 ]
        comboset fpga.cam.val [ expr $val_addent1 + $val_addent2 ]
}
.
.
.
```